

ABSTRACT

A plasma display panel is disclosed. The plasma display panel includes: a pair of panels facing at a prescribed interval from each other; sustain electrodes having a plurality of transparent electrodes arranged on one of the panels and bus electrodes formed to be at least partially overlapped on the transparent electrodes, the sustain electrodes being in pairs; address electrodes arranged to intersect the pairs of sustain electrodes; a plurality of cells formed on intersecting points of the pairs of sustain electrodes and the address electrodes; barrier formed between the panels for dividing the cells; and fluorescent layers arranged between the barrier, wherein a light absorption layer for absorbing light of each fluorescent layer formed on the cell is provided on a side of each bus electrode directing the inside of the cell. Therefore, the plasma display panel can prevent light emitted from the fluorescent layer from being reflected by a side of the bus electrode again when ultraviolet rays excite the fluorescent layer of the inside of the cell and the light is emitted from the fluorescent layer, thereby improving color purity and contrast.